Abstracts

Efficient integral equation formulations for admittance or impedance representation of planar waveguide junctions

G. Gerini, M. Guglielmi and G. Lastoria. "Efficient integral equation formulations for admittance or impedance representation of planar waveguide junctions." 1998 MTT-S International Microwave Symposium Digest 98.3 (1998 Vol. III [MWSYM]): 1747-1750.

In this paper we describe very efficient impedance and admittance Integral Equation (IE) formulations for the study of passive microwave devices composed of cascaded uniform waveguide sections. The formulation leads directly to reduced multimode matrix representations that only involve a small number of accessible, or interacting, modes. In addition to theoretical results, a performance comparison is also discussed which clearly demonstrates the improvement achieved.

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